

DERWENT-ACC-NO: 2002-605172

DERWENT-WEEK: 200265

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TITLE: Plasma display panel and method for manufacturing plasma display panel

INVENTOR: MIN, B G

PATENT-ASSIGNEE: LG ELECTRONICS INC[GLDS]

PRIORITY-DATA: 2000KR-0047030 (August 14, 2000)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
KR 2002013325 A	February 20, 2002	N/A	001	H01J 017/49

APPLICATION-DATA:

PUB-NO	APPL-DESCRIPTOR	APPL-NO	APPL-DATE
KR2002013325A	N/A	2000KR-0047030	August 14, 2000

INT-CL (IPC): H01J017/49

ABSTRACTED-PUB-NO: KR2002013325A

BASIC-ABSTRACT:

NOVELTY - A PDP and a method for manufacturing the PDP are provided to reduce an additional process and the number of mask on manufacturing the PDP and prevent cells from being mis-discharged by blocking an electrical and optical interference among adjacent cells.

DETAILED DESCRIPTION - An upper substrate of PDP includes a scan/sustain electrode(70), a common sustain electrode(50) and a pair of trigger electrodes(57,63). A black stripe(75) is formed between the trigger electrodes(57,63). The scan/sustain electrode(70), the common sustain electrode(50) and the trigger electrodes(57,63) are composed of transparent electrodes(44,52,58,65) and bus electrodes(48,56,62,69). The bus

electrodes(48,56,62,69) is formed successively with Ti-layers(45,53,59,66), Cu-layers(46,54,60,67) and Ag-layers(47,55,61,68) on the transparent **electrodes**(44,52,58,62).

CHOSEN-DRAWING: Dwg.1/10

TITLE-TERMS: PLASMA DISPLAY PANEL METHOD MANUFACTURE PLASMA
DISPLAY PANEL

DERWENT-CLASS: V05

EPI-CODES: V05-A01A3; V05-A01C2;

